Attorney Docket No.: CYPR-CD00183



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Theres has no	₹/		al of the below designation and the below de					,		n envelope eposit.
7 & Defe of Deposit	11/1	4/02 Nar	ne of Person king the Deposit:	KATHERINE		Signature Making the	of the Persor	Kathe	sine 1	Rinaldi
In re	Applicati	on of: War	ren Snyder					. •	,	,
Seria	Serial No.: 09/975,104 Examiner: not yet assigned									
Filed:	10/1	0/01			Art	Unit: 21	184			
	For: Capturing test/emulation and enabling real-time debugging using an FPGA FOR IN-CIRCUIT EMULATION									
	stant C		oner for Pat 20231	tents						\C\\/CD
				AMEN	DMENT TI	RANSMIT	TAL			EIVED
1	Trans	emitted her	, owith is an am	endment fo	r this annli	cation			NOV	2 2 2002
1.	Transmitted herewith is an amendment for this application     Technology Center 2100									
х -	x Transmitted herewith is an amendment for the above identified patent application.									
х .	( 9 sheets)  X Transmitted herewith are 9 sheets of Supporting Documents A-D.									
***************************************			(**************************************	•••••						
2.	2. Applicant is other than a small entity									
				Exten	sion of	Term				
3.	The p	roceeding	s herein are fo	r a patent a	application	and the	orovisions	of 37 C.F	F.R. 1.136	apply.
(a)	[]		nt petitions for 7 C.F.R. 1.17(						ow:)	
			Extension		<u>Fe</u>					
			[ ] one month [ ] two months			10.00 00.00				
			[ ] three mont	ths	\$9	20.00				
			[ ] four month	S	\$1	,960.00				
					<u>F e</u>	e \$				

If an additional extension of time is required, please consider this a petition therefor.

(b) [X] Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

Attorney Docket No.: CYPR-CD00183

## Fe Calculation

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for ther than a	small ntity)					
Fee Items	Claims Of Claims Remaining After Amendment For		Present Extra Claims	Fee Rate	Total	
Total Claims	17	- 20 =	0	x \$18.00	\$0.00	
Independent Claims	3	- 3 =	0	<b>x</b> \$80.00	\$0.00	
Multiple Dependent Claim Fee (one or more, first added by this \$260.00 amendment)						
Total Fees						

## **PAYMENT OF FEES**

5.	The full fee due in connection with this communication is
	provided as follows:

[ x ]	The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.  A <u>duplicate copy</u> of this authorization is enclosed.
r ı	A check in the amount of ¢

[	]	Charge any fee	s required or	credit any	overpayments	associated with	n this filing to	Deposit
		Account No.:	23-0085.				•	

Please direct all correspondence concerning the above-identified application to the following address:

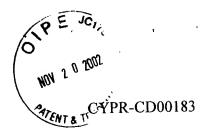
## WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060

Respectfully submitted,

Date: /// / Y/ Zero >

Anthony C. Murabito Reg. No. 35,295



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Warren SNYDER

: GROUP ART UNIT:

SERIAL NO: 09/975,104

FILED: OCTOBER 10, 2001

FOR: CAPTURING TEST/EMULATION AND ENABLING REAL-TIME DEBUGGING USING AN FPGA FOR IN-CIRCUIT EMULATION

RECEIVED

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PRELIMINARY AMENDMENT

**Technology Center 2100** 

COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following Amendments and Remarks is respectfully requested.

## IN THE SPECIFICATION

Please replace the paragraph at page 1, lines 12-28 with the following paragraph:

This application is related to U.S. Patent Application Serial No. 09/975,115, docket number CYPR-CD00182, to Warren Snyder, et al., entitled "IN-SYSTEM CHIP EMULATOR ARCHITECTURE"; and to U.S. Patent Application Serial No. 09/975,030, docket number CYPR-CD00185, to Warren Snyder, et al., entitled "EMULATOR CHIP/BOARD ARCHITECTURE AND INTERFACE"; and to U.S. Patent Application Serial No. 09/975,338, docket number CYPR-CD00186, to Warren Snyder, et al., entitled "METHOD FOR BREAKING EXECUTION OF (TEST) CODE IN A DUT AND EMULATOR CHIP ESSENTIALLY SIMULTANEOUSLY AND HANDLING COMPLEX BREAKPOINT